



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: John L. Melanson Attorney Docket No. 021615.500530 [1064-CA]
Serial No.: 10/018,819
Filing Date: December 6, 2001
Group Art Unit: 2631
Examiner Name: Not Assigned
Title: DIRECT SYNTHESIS CLOCK GENERATION CIRCUITS AND METHODS

STATUS INQUIRY

Commissioner of Patents and Trademarks
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The last paper received from the U.S. Patent and Trademark Office in connection with the above-referenced matter is a letter from the Commissioner for Patents (dated April 9, 2003) stating that it would be more than 15 months before this application would be examined. We have received no further papers or correspondence from the USPTO on this application. Applicant respectfully requests a status update on the subject application.

OCT 14 2004
U.S. TRADEMARK OFFICE

CERTIFICATE OF MAILING UNDER 37 CFR § 1.8(a)

I hereby certify that this correspondence, including all of the listed attachments, is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria Virginia, 22313-1450 on this date.

Name: Susan Turner

Date: October 11, 2004

Respectfully submitted,

DATE: 10-11-04

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